

EV317195623



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No
Filing Date August 16, 2001
Inventor
Assignee Micron Technology, Inc.
Group Art Unit
Examiner R. Rocchegiani
Attorney's Docket No
Title: Semiconductor Processing Methods, and Semiconductor Assemblies

RESPONSE TO FEBRUARY 4, 2003 OFFICE ACTION

To:

Mail Stop Fee-Amendment Commissioner for Patents

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From:

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Responsive to the Office Action dated February 4, 2001, Applicant amends and remarks as follows:

AMENDMENTS

<u>Underlines</u> indicate insertions and strikeouts indicate deletions.